

IN THE CLAIMS:

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1           1.       (Original) A multilayer board, comprising  
2                   a signal line requiring tamper-resistance, the signal line including: (a) a  
A 3       conductive trace and (b) a conductive via that passes through layers of the multilayer board,  
4       wherein  
5                   the conductive trace and an end of the conductive via existing on an outside layer  
6       of the multilayer board are placed under one or more circuit components mounted on the outside  
7       layer.

1           2.       (Original) The multilayer board of Claim 1, wherein  
2                   the signal line further includes a conductive trace on an inner layer that is  
3       sandwiched between sheets of foil and/or circuit components placed on layers above and below  
4       the inner layer so that the sheets of foil and/or circuit components hide the conductive trace on  
5       the inner layer when viewed from above or below.

1           3.       (Original) The multilayer board of Claim 2, wherein  
2                   the sheets of foil placed on the layers that are outside the inner layer are  
3       connected to either a ground or a power source.

1           4.       (Original) The multilayer board of Claim 3, wherein  
2                   the conductive trace on the outside layer is further covered by a circuit component  
3       on another outside layer when viewed from above or below.

1           5.     (Original) The multilayer board of Claim 2, wherein  
2                   the signal line requiring tamper-resistance is either a signal line that is input to an  
3 encryption unit or a signal line that is output from a decryption unit.

1           6.     (Original) A multilayer board, comprising:  
2                   a certain signal line that includes (a) a conductive trace and (b) a conductive via  
3 that passes through layers of the multilayer board, wherein  
4                   the conductive trace and an end of the conductive via existing on an outside layer  
5 of the multilayer board are placed under one or more circuit components mounted on the outside  
6 layer,  
7                   the certain signal line further includes a conductive trace on an inner layer that is  
8 sandwiched between sheets of foil and/ or circuit components placed on layers above and below  
9 the inner layer so that the sheets of foil and/or circuit components hide the conductive trace on  
10 the inner layer when viewed from above or below, and  
11                  the certain signal line is either a data line or an address line.

1           7-29.   (Cancelled)

1           30.    (New) A tamper-resistant multilayer board for transfer of pixel data to be  
2 encrypted comprising:  
3                   a board member having a plurality of layers and one or more components  
4 mounted thereon;  
5                   a reception/decryption unit mounted on the board member;

6 an output interface unit mounted on the board member and operatively connected  
7 to the reception/decryption unit; and  
8 a conductive path operatively designed for interconnecting the reception/  
9 decryption unit and the output interface unit and position adjacent an interior layer surface for a  
10 portion of the conductive path and positioned under one or more components for the remainder  
11 of the conductive path to prevent direct access from the exterior of the board member.

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